

# CMPE 121/L

## Fall 2015

### Policies and course organization

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## Instructor and TA

- Instructor:
  - ❖ Anujan Varma, Professor, Computer Engineering
  - ❖ Office hours: Fri 11:00 – 12:00
- Teaching Assistant:
  - ❖ Sam Mansfield
- Tutors:
  - ❖ TBA

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## Web pages

- ❑ Located at
  - [cmpe121-fall15-01.courses.soe.ucsc.edu/home](http://cmpe121-fall15-01.courses.soe.ucsc.edu/home)
- ❑ May set up calendar feed for schedule
- ❑ Use Class Forum for discussions
  - ❖ Integrated with Website
- ❑ Login with CruzID Blue
- ❑ CMPE121L eCommons site used for lab report submissions

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## Course pre-requisites

- ❑ CMPE 12/L
- ❑ CMPE 100/L
- ❑ EE 101/L
- ❑ CMPE 13/L
  
- ❑ Knowledge of C programming

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## Registration

- Must register for 121 and one section of 121L
  - ❖ No option to take them separately

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## Lab

- Located in BE 161
- All registered students have 24-hour access
- 5 Exercises
  - ❖ Done individually
  - ❖ Materials posted 2 weeks before submission deadline
  - ❖ Exercises due every week
  - ❖ Check-off due in lab section
  - ❖ Lab report due on Fridays before 11:55 PM
    - Late reports lose 25% of grade per day
    - No credit after 3 days
  - ❖ All work to be done independently
- Lab project

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## Lab Kits

- Exercises based on Cypress CY8CKIT-050 LP Microcontroller kit
- Kits being distributed in lab this week
- Bag of parts also to be distributed in first week
- Some soldering may be required for final project.

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## Software Tools

- Cypress PSoC Creator for hardware/software design
- Eagle for schematics
- Energia for CC3200 WiFi controller

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## Lab Rules

- Absolutely no food and drinks in the lab
- Follow safety rules
  - ❖ Wear eye protection when soldering
- You will be given your own Cypress PSoC 5 Development Kit and a bag of parts
- Keep a lab notebook
  - ❖ Schematics must be completed before wiring circuits
- Keep data-sheets/app notes handy in a binder
- Keep notes as you perform experiments
- Be courteous to other students
  - ❖ Registered students have priority at scheduled section times

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## Lab Safety

- Disconnect power before making changes to your circuits
- No hanging wires
- Do not short IO pins
- Secure wires (electrical tape works)
- Be careful when using 12V power
- Pay attention to power supply and I/O voltage specifications (5V versus 3.3V)
- Pay attention to current capacities of GPIO pins
- Common ground
  
- BELS will charge you for replacement boards and parts

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## Lab Checkoffs

- Must be done in the registered lab section
- You must get the TA's signature in your notebook for each exercise.

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## Lab Reports

- Due on Fridays
- Submit through eCommons
- Feedback and grades posted on eCommons

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## What to Submit?

- ❑ Your source code
- ❑ Schematics (pdf)
- ❑ Documentation (pdf or text)
- ❑ Do not submit generated files:
  - ❖ Generated\_source
  - ❖ CortexM3
  - ❖ codegentemp
- ❑ Place report and other documents in “doc” directory
- ❑ Submit single zip file “your-name.zip”

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## Grade Allocation

- ❑ Theory: 50%
  - ❖ Midterm: 15%
  - ❖ Final: 15%
  - ❖ Homeworks: 10%
  - ❖ Quizzes, class participation: 10%
- ❑ Practice: 50%
  - ❖ Lab exercises: 30%
  - ❖ Lab project 20%
- ❑ You will receive the same grade for CE 121 and 121L

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## Honor Code

- ❑ All submitted material must be independent work
  - ❖ Homework solutions
  - ❖ Lab reports
  - ❖ Lab project
- ❑ Do not share your designs and code with others
- ❑ Violations of academic integrity will be handled according to university policies [www.ue.ucsc.edu/academic\\_misconduct](http://www.ue.ucsc.edu/academic_misconduct)

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## DRC Resources

*If you qualify for classroom accommodations because of a disability, please get an Accommodation Authorization from the Disability Resource Center (DRC) and submit it to me in person outside of class (e.g., office hours) within the first two weeks of the quarter. Contact DRC at 459-2089 (voice), 459-4806 (TTY), or <http://drc.ucsc.edu> for more information on the requirements and/or process.*

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